



# गोंय विद्यापीठ

ताळगांव पठार

गोंय - ४०३ २०६

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(Accredited by NAAC)

## Goa University

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GU/Acad –PG/BoS -NEP/2023/85/4

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### CIRCULAR

In supersession to the above referred Circular, the updated approved Syllabus with revised Course Codes of the **Master of Science in Electronics** Programme is enclosed.

The Dean/ Vice-Deans of the School of Physical and Applied Sciences are requested to take note of the above and bring the contents of the Circular to the notice of all concerned.

(Ashwin Lawande)

Assistant Registrar – Academic-PG

To,

1. The Dean, School of Physical and Applied Sciences, Goa University.
2. The Vice-Deans, School of Physical and Applied Sciences, Goa University.

Copy to:

1. The Chairperson, Board of Studies in Electronics.
2. The Programme Director, M. Sc. Electronics, Goa University.
3. The Controller of Examinations, Goa University.
4. The Assistant Registrar, PG Examinations, Goa University.
5. Directorate of Internal Quality Assurance, Goa University for uploading the Syllabus on the University website.

**Goa University**

**M.Sc. Electronics Programme Course Syllabus as per NEP 2020**

Semester	Course Code	Course Title	Theory/ Lab	Course Credits	Contact Hours
<b>Semester I</b>	<b>Discipline Specific Core Courses (DSCC)</b>				
	<a href="#">ELE-500</a>	Micro Electronics and VLSI Design	Theory	4	60
	<a href="#">ELE-501</a>	Instrumentation & Control Theory	Theory	4	60
	<a href="#">ELE-502</a>	Advanced Digital Communication System	Theory	4	60
	<a href="#">ELE-503</a>	Electronics Practical I	Lab	4	120
	<b>Discipline Specific Elective Courses (DSEC)</b>				
	<a href="#">ELE-521</a>	Numerical Computation and Algorithms	Theory	4	60
	<a href="#">ELE-522</a>	EDA Tools	Theory	4	60
<b>Semester II</b>	<b>Discipline Specific Core Courses (DSCC)</b>				
	<a href="#">ELE-504</a>	Embedded System Design	Theory	4	60
	<a href="#">ELE-505</a>	Real Time Operating System	Theory	4	60
	<a href="#">ELE-506</a>	Digital System Design	Theory	4	60
	<a href="#">ELE-507</a>	Electronics Practical II	Lab	4	120
	<b>Discipline Specific Elective Courses (DSEC)</b>				
	<a href="#">ELE-523</a>	Internet of Things	Theory	4	60
	<a href="#">ELE-524</a>	Switching and Routing	Theory	4	60
<b>Semester III</b>	<b>Research Specific Elective Courses (RSEC)</b>				
	<a href="#">ELE-600</a>	Signals and Systems	Theory	4	60
	<a href="#">ELE-601</a>	Artificial Intelligence and Applications	Theory	4	60
	<a href="#">ELE-602</a>	Robotics	Theory	4	60
	<b>Elective Generic Course (EGC)</b>				
	<a href="#">ELE- 621</a>	Laser System Engineering	Theory	4	60
	<a href="#">ELE- 622</a>	Data Science and Machine Learning	Theory	4	60
	<a href="#">ELE- 623</a>	Electric Vehicle Technology	Theory	4	60
	<a href="#">ELE- 624</a>	Biomedical Instrumentation	Theory	4	60
<b>Semester IV</b>	<b>Research Specific Elective Courses (RSEC)</b>				
	<a href="#">ELE-603</a>	Optical Communication Systems	Theory	4	60
	<a href="#">ELE-604</a>	Digital Image Processing	Theory	4	60
	<a href="#">ELE-605</a>	Neuromorphic Computing	Theory	4	60
	<b>Discipline Specific Dissertation (DSD)</b>				
	<a href="#">ELE-625</a>	Project	Dissertation	16	480

## Semester I

**Course Code: ELE-500**

**Course Title: Micro Electronics and VLSI Design**

**Number of Credits: 04**

**Total Hours: 60**

**Total Marks: 100**

**Effective from AY: 2022-23**

<b>Prerequisites for the course</b>		
Should have graduate level knowledge in analog and digital electronics		
<b>Objectives of Course</b>		
This course is intended to: <ul style="list-style-type: none"><li>● Introduce to the VLSI Technology, various fabrications processes involved in IC design ,</li><li>● Analysis of Electronics circuits, Design examples of VLSI circuits, Circuit Optimization techniques</li><li>● Advance circuits designs: Memory, Registers, Synchronous circuits etc.</li></ul>		
<b>Course Content</b>		
<b>Unit I</b>	An overview of VLSI, Modern CMOS Technology	<b>4 Hours</b>
<b>Unit II</b>	Silicon Logic, Logic design with MOSFET.	<b>6 Hours</b>
<b>Unit III</b>	Physical structure of CMOS Integrated circuits	<b>6Hours</b>
<b>Unit IV</b>	Fabrication Technologies of CMOS Integrated Circuits	<b>7 Hours</b>
<b>Unit V</b>	Elements of Physical Design	<b>4 Hours</b>
<b>Unit VI</b>	Electrical characteristics of MOSFETS	<b>6 Hours</b>
<b>Unit VII</b>	Electronic analysis of CMOS Logic gates	<b>6 Hours</b>
<b>UNIT VIII</b>	Advanced Techniques in CMOS Logic Circuits	<b>6 Hours</b>
<b>UNIT IX</b>	System specifications using HDL, General VLSI	<b>5 Hours</b>

	components	
<b>UNIT X</b>	Memories and Programmable Logic	<b>10 Hours</b>
<b>Pedagogy</b>		
Lectures/Experiential Learning		
<b>Course Outcome</b>		
<p>Students will,</p> <ul style="list-style-type: none"> <li>● Design fundamental gates and customize them for specific electrical and electronics application,</li> <li>● Understand the fabrications processes involved in VLSI technology,</li> <li>● Write the Hardware descriptive form of circuits, Synchronize the combinational and sequential circuits, design a static and dynamic memory cell,</li> <li>● Understand the Programmable logics building blocks</li> </ul>		
<b>References/Readings</b>		
<ol style="list-style-type: none"> <li>1. Introduction to VLSI Circuits and Systems, John P. Uyemura, WILEY.</li> <li>2. Principles of CMOS VLSI Design, N.H.E. W. &amp; Eshahiraghian, Addison Wesley</li> <li>3. Modern VLSI Design System on Silicon, Pearson Education Asia. By W. Wolf.</li> <li>4. VLSI Technology, S.M. Sze, McGraw -Hill (1995).</li> <li>5. Basic VLSI Design, Douglas Pucknell, K. Eshraghian, Prentice Hall India.</li> </ol>		

**Course Code: ELE-501**

**Course Title: Instrumentation & Control Theory**

**Number of Credits: 04**

**Total Hours: 60**

**Total Marks: 100**

**Effective from AY: 2022-23**

<b>Prerequisites for the course</b>		
knowledge of analog and digital electronics, Basics of differential equations.		
<b>Objectives of Course</b>		
This course is intended to: <ul style="list-style-type: none"><li>● Introduce principles of transduction and actuator.</li><li>● Explain the important parameters used in instrument characterization, Types of error committed by a user and how to deal with them.</li><li>● Describe various standards followed for accurate measurement.</li><li>● Explain the techniques used to convert analog data into digital domain and its analysis and storage.</li><li>● Understand instruments such as Oscilloscope, spectrum analyzers, wave analyzers, Lock in amplifiers depth.</li><li>● Fundamentals of control theory and working of PID controller tuned for a given application.</li></ul>		
<b>Course Content</b>		
<b>Unit I</b>	<b>Introduction</b>	<b>8 Hours</b>
Basic Concepts of measurements, Calibrations and standards, Transducers: Types and parameters, Sensors - Displacement, Strain, Vibration, Pressure, Flow, Temperature, Force and Torque		
<b>Unit II</b>	<b>Signal Conditioning</b>	<b>8 Hours</b>
Introduction, Amplification, Simple ended amplifier, Instrumentation amplifier, Types of Filters		
<b>Unit III</b>	<b>Sampling</b>	<b>8 Hours</b>
Fundamental concepts, Anti-aliasing, Multiplexers, Sample and Hold, Track and Hold.		
<b>Unit IV</b>	<b>Computer Interfaces</b>	<b>08 Hours</b>
Serial (RS-232), Parallel, GPIB (IEEE-488), Universal Serial Bus (USB) and Variants, Bluetooth		
<b>Unit V</b>	<b>Display Devices</b>	<b>10 Hours</b>
Review of LED, LCD, Plasma display devices, segmental and dot matrix displays, MEMS display		
<b>Unit VI</b>	<b>General Purpose Test Equipments</b>	<b>08 Hours</b>

CRO, Digital storage oscilloscope, Digital voltmeter, Wave Analyser, Spectrum analysis, Lock-in-amplifiers, Pulse generators and waveform generators		
<b>Unit VII</b>	<b>Control System</b>	<b>10 Hours</b>
Types of control system - open loop, closed loop, linear, non-linear, continuous, discrete, frequency and time response, open loop motor control, Principles of PD, PI, PID		
<b>Pedagogy</b>		
Lectures/Experiential Learning		
<b>Course Outcome</b>		
students will: <ul style="list-style-type: none"> <li>● Explain measurement parameters, calibrations and standards in electronic instrumentation.</li> <li>● Comprehend the significance of signal conditioning and sampling theorem.</li> <li>● Gain the knowledge of various computer interfaces, and understand the construction, working principle of different display devices and general-purpose equipments used in signal analysis.</li> <li>● Explain the working principle of different types of control systems.</li> </ul>		
<b>References/Readings</b>		
1. H. S. Kalsi, 'Electronic Instrumentation', Tata MacGrow-Hill 2. Joseph J. Carr, 'Elements of Electronic Instrumentation and Measurement', Prentice Hall India. 3. Albert Helfnick and William Cooper, 'Modern Electronic Instrumentation and Measurement Techniques', Prentice Hall India. 4. Robert Northrop, 'Introduction to Instrumentation and Measurements', CRC Press		

[\(Back to Index\)](#)

**Course Code: ELE-502**

**Course Title: ADVANCED DIGITAL COMMUNICATION SYSTEMS**

**Number of Credits: 04**

**Total Hours: 60**

**Total Marks: 100**

**Effective from AY: 2022-23**

<b>Prerequisites for the course</b>		
Graduate level understanding in basics of Electronic Communications		
<b>Objectives of Course</b>		
This course is intended to: <ul style="list-style-type: none"><li>● Introduce to students with basics of wireless systems – concepts, theory.</li><li>● Covers various modulation techniques, to enable the student to synthesize and</li><li>● analyse wireless and mobile cellular communication systems over a stochastic fading channel</li><li>● Mitigation and diversity techniques</li></ul>		
<b>Course Content</b>		
<b>Unit I</b>	<b>Introduction to Mobile and Cellular Communication Systems:</b>	<b>5 Hours</b>
Definitions, impact of Mobile and Cellular Radio Communication Historical overview. Fundamental of Radio Mobile and Cellular Practices Radio mobile links and cells, Frequency re-use, Principles of Cellular Com. Mobile Telephone Switching Subsystem, The mobile frequency spectrum, Hand-off, Cochannel and adjacent channel interference limitations, Near-far problem, Power Control.		
<b>Unit II</b>	<b>Mobile Communication Channel including antennas</b>	<b>10 Hours</b>
The mobile wireless propagation channel, Notions on antennas especially the near and far field concept, Line of Sight (LOS) propagation, Multipath fading , outdoor and Indoor Propagation, Flat and selective fading, Special antennas for base stations and headsets, Deterministic, Empirical and Statistical Methods for propagation link computations.		
<b>Unit III</b>	<b>Overview of Mobile and Cellular Radio Communication Modulation and Detection Techniques:</b>	<b>15 Hours</b>
Analog modulations and detection: AM, FM, PM, ACSB, Hybrid and Digital modulation: PCM, ASK, FSK, QPSK, QAM, MSK, etc, Coherent and noncoherent detection, C/N, S/N, Eb/No and BER relations, Probability concepts, Mobile Radio links parameters.		
<b>Unit IV</b>	<b>Overview of Multiple Accesses Techniques</b>	<b>12 Hours</b>
Simplex, Duplex TDD and Time Division Duplex, Time division multiple access (TDMA) FDMA and OFDM, Code Division multiple access (CDMA), Hybrid multiple access, Management of voice, Data and Video (Multimedia) information.		
<b>Unit V</b>	<b>Modern Digital Radio Systems</b>	<b>10 Hours</b>
standards, proposals and comparisons GSM (Europe and all over the world) - TDMA, IS-54 (U.S.A.)- TDMA, IS-95 (U.S.A., Korea) CDMA-, PHS (Japan) - TDMA, Frequency Hopping (FH) (U.S.A.) - CDMA, PCS, PCS Cordless telephone 2nd generation (CT-2), Cellular digital packet data (CDPD), and Wireless LAN, New standard trends Edge, 3rd and 4th generation beginning, LTE,		
<b>Unit VI</b>	<b>Mitigation Techniques for Mobile System</b>	<b>4 Hours</b>
Overview of Natural and manmade external noise sources, Radiation hazards effects from		

base stations, Mobile and portable equipments.

<b>Unit VII</b>	<b>Diversity Techniques for Mobile Radio Systems</b>	<b>4 Hours</b>
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Dispersive channels, Space diversity, Frequency diversity, Equalizer techniques

**Pedagogy**

Lectures/Experiential Learning

**Course Outcome**

Students will

- understand the design, specifications and the performances of various wireless communication systems
- Apply the cellular concepts to evaluate the signal reception performance in a cellular network.
- Apply the traffic analysis to design cellular network with given quality of service constraints.
- Determine the appropriate model of wireless fading channel based on the system parameters and the property of the wireless medium.
- Analyze and design receiver and transmitter diversity techniques.

**References/Readings**

1. Steele, R., Hanzo, L., "Mobile Radio Communication" 3rd Edition Wiley 2005.
2. Rappaport, T.S., "Wireless Communications: Principles And Practice, 2/E, Pearson
3. Wireless Communications (WIRELESS COMMUNICATIONS, 2ND ED, Molisch A F), Wiley



**Course Code: ELE-503**

**Course Title: ELECTRONICS PRACTICALS – I**

**Number of Credits: 04**

**Total Hours: 60**

**Total Marks: 100**

**Effective from AY: 2022-23**

<b>Prerequisites for the course</b>	
Should have studied graduate level basic level electronic subject. It is assumed that students have a working knowledge of passive and active components and digital circuits.	
<b>Objectives of Course</b>	
This course is intended to: <ul style="list-style-type: none"><li>● Give the hands-on experience to design the basic digital and analog circuits</li><li>● Simulate the various digital modulation techniques and data correction and detection used in general communication system.</li><li>● Expose students to design digital circuits using microwind.</li><li>● Implement numerical algorithm.</li></ul>	
<b>Course Content</b>	
<b>Practical Title</b>	<b>120 Hours</b>
<b><u>Unit I</u></b> <ol style="list-style-type: none"><li>1. Design of counters for digital clock (using Microwind s/w )</li><li>2. Multiplexer and Demultiplexer (using Microwind s/w )</li><li>3. Encoder and Decoder (using Microwind s/w )</li><li>4. 2nd order Butter-worth Notch Filter (p-Spice)</li><li>5. Buffer design using SPICE (p-Spice)</li><li>6. Memory design using 6T cell</li></ol>	
<b><u>Unit II</u></b> <ol style="list-style-type: none"><li>7. Design of variable voltage supply @ 2 Amps.</li><li>8. Design of Function Generator.</li><li>9. Design of Power Amplifier 10 Watts.</li><li>10. Design of Stepper driver using Monoshot &amp; 555 Timer.</li><li>11. Design of S/C circuit for Strain gauge /Glucose strip @ 3.3V.</li><li>12. Design of 4-bit UP-DOWN Counter.</li></ol>	
<b><u>Unit III</u></b> <ol style="list-style-type: none"><li>13. Implementation of MSK modulation and demodulation.</li><li>14. ASK, FSK, QPSK modulation &amp; demodulation.</li><li>15. QPSK, modulation &amp; demodulation</li><li>16. DS-CDMA simulation.</li><li>17. Channel Coding methods. a. Convolution b. Block code</li><li>18. Error detection and correction Algorithm<ol style="list-style-type: none"><li>a. CRC</li><li>b. Hamming code</li></ol></li></ol>	

**Unit IV**

19. Numerical Programming 1 (Trapezoid method)
20. Numerical Programming 2 (Bisection method)
21. Numerical Programming 3 (Runge Kutta method)
22. Numerical Programming 4 (Newton Raphson method)
23. Numerical Programming 5 (Regula falsi method)
24. Numerical Programming 6 (Secant method)

**Pedagogy**

Presentations /assignments/Experiential learning

**Course Outcome**

The Students will :

- learn the basics of a communication system for modulation, data coding , error coding channel coding methods.
- Design signal conditioning and VLSI circuits for various applications.

**Course Code: ELE-521**

**Course Title: Numerical Computation and Algorithms**

**Number of Credits: 04**

**Total Hours: 60**

**Total Marks: 100**

**Effective from AY: 2022-23**

<b>Prerequisites for the course</b>		
<b>Students should have a knowledge of programming</b>		
<b>Objectives of Course</b>		
<b>The course is intended to,</b> <ul style="list-style-type: none"><li>● <b>Develop the basic understanding of numerical computation and algorithm.</b></li><li>● <b>Develop skills to implement algorithms to solve mathematical problems on the computer and Data Bases.</b></li></ul>		
<b>Course Content</b>		
<b>Unit I</b>	<b>Computer Programming</b>	<b>8 Hours</b>
Introduction to Algorithms, Elements of Computer Programming language, Basics of algorithm design, general model, Dynamic programming model, principle of optimality, backtracking models.		
<b>Unit II</b>	<b>Data Structures</b>	<b>12 Hours</b>
Introduction to Data Structures, Vectors and Lists, Stack, Queue, Binary Trees, Graphs, Hashing.		
<b>Unit III</b>	<b>Theory of Numerical programming</b>	<b>25 Hours</b>
Numerical Integration: Theory of numerical errors, Trapezoidal & Simpson's rule, Romberg method, Improper integrals; Numerical Solution of linear equations: Gauss-Jordan elimination and Lu decomposition; Numerical Solutions of nonlinear equations: Bracketting, bisection, Secant & Regula falsi method, Newton Raphson method; Numerical Solutions to Ordinary differential equations: Runge-Kutta method, Modified midpoint method, Richardson extrapolation.		
<b>Unit IV</b>	<b>Database</b>	<b>15 Hours</b>
Basic Concepts, Relational Data Model, Database Design, DBMS storage structures and access methods, Query Processing, Transaction Processing, Security & Integrity, Distributed Databases, Client Server Computing		
<b>Pedagogy</b>		

lectures/ Experiential Learning

### **Course Outcome**

The students will:

- Use numerical methods for solving a problem, locate and use good mathematical software to achieve the required accuracy for a particular application, get the accuracy you need from the computer, assess the reliability of the numerical results.
- Solve a linear system of equations using an appropriate numerical method.
- Use data structures like Lists, Stack, Queue, Binary Trees.

### **References/Readings**

1. Data structures using C and C++ by Yedidiah Langsam, Moshe J Augenstein, Aaron M Tenenbaum, Prentice Hall of India, 1995
2. Data Abstraction and Problem solving in Java by Frank M Carrano, Janet J Prichard ,Addison-Wesley, 2001
3. Numerical Recipes in C, William H. Press, Brain P. Flannery, William T. Vetterling, Saul A. Teulosky, Cambridge University Press, 1990.
4. Numerical Mathematical Analysis, J. B. Scarborough, Oxford and IBM Publishing Company (1979).
5. Numerical Recipes in C: The Art of Scientific Computing by William H Press, Brian P Flannery, Saul A Teukolsky - Mathematics – 1992.
6. Fundamentals of Database Systems, 4th Edition by R Elmasri, S Navathe Addison-Wesley, 2003

**Course Code:** ELE-522

**Course Title:** EDA Tools

**Number of Credits:** 04

**Total Hours:** 60

**Total Marks:** 100

**Effective from AY:** 2022-23

<b>Prerequisites for the course</b>
Should have studied Digital Communication Systems
<b>Objectives of Course</b>
<p>This course is intended to:</p> <ul style="list-style-type: none"><li>● Familiarize the students with industry oriented EDA tools.</li><li>● Teach Quartus, ISE compilations and programming and its use for design and analysis.</li><li>● Enable the student to extract various design parameters from simulation results.</li></ul>
<b>Course Content (minimum 10 to be studied/implemented)</b>
<p>Study of JTAG, Modelsim Syntax study.</p> <ol style="list-style-type: none"><li>1. Study of Phases of Quartus compilations.</li><li>2. Study of phases of ISE compilations</li><li>3. Testing logic using ChipScope-I.</li><li>4. Testing logic using ChipScope-II</li><li>5. Parallel implementation of CRC.</li><li>6. Serial implementation of CRC.</li><li>7. FIFO implementation</li><li>8. Pulse stretcher</li><li>9. Test bench using Modelsim-I</li><li>10. Test bench using Modelsim-I</li><li>11. Test bench using Modelsim-I</li><li>12. Test bench using Modelsim-I</li></ol>
<b>Pedagogy</b>
Lectures/FLIPPED CLASSROOM/Experiential Learning
<b>Course Outcome</b>
<p>The students will,</p> <ul style="list-style-type: none"><li>● Perform compilation using Quartus and ISE software.</li><li>● Analyse logic using Chipscope-I and II.</li><li>● Develop the Test benches using Modelsim-I</li></ul>
<b>References/Readings</b>

1. Design through Verilog HDL By T. R> Padmanabhan & Sundari. IEEE press, Wiley Interscience.
2. [http://www.xilinx.com/itp/xilinx7/help/iseguide/html/ise\\_fpga\\_design\\_flow\\_overview.htm](http://www.xilinx.com/itp/xilinx7/help/iseguide/html/ise_fpga_design_flow_overview.htm)
3. Hands on experience on altera development board by J.S.Parab,etal: Springer Netherland 2018(ISBN 978-81-322-3769-3)

## SEMESTER II

**Course Code: ELE-504**

**Course Title: EMBEDDED SYSTEMS DESIGNS**

**Number of Credits: 04**

**Total Hours: 60**

**Total Marks: 100**

**Effective from AY: 2022-23**

**Prerequisites for the course**

Should have studied microprocessor and C programming at graduate level

### **Objectives of Course**

This course is intended to:

- Introduce with Architectures of Microcontroller and its programming with Interfacing various Interfaces is discussed in depth in this paper.
- Programming in assembly as well as in C for 8/16/32 bit controller

### **Course Content**

<b>Unit I</b>	<b>Introduction to Controller Architecture</b>	<b>5 Hours</b>
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Computer Architecture, RISC/CISC and Princeton Architectures

<b>Unit II</b>	<b>Embedded system</b>	<b>5</b>
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Definition, Basic Block, Designing of System, Applications

<b>Unit III</b>	<b>8-bit Micro controllers</b>	<b>20</b>
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Introduction to various 8-Bit microcontroller, 8051 features, Architecture , Memory organization, Instruction set, Interrupts, Timer/counter, LED, Switches, ADC, DAC, LCD Interfacing, Programming in Assembly and C,

<b>Unit IV</b>	<b>16 bit microcontroller</b>	<b>15</b>
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PIC controller Introduction, Architecture, Instruction set, Peripheral interfaces: LED, LCD, Serial RS232, Programming in C

<b>Unit V</b>	<b>32-bit Microcontroller</b>	<b>15</b>
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ARM architecture, THUMB/ARM instruction, ARM Exception Handling, Timers/Counters, UART, SPI, PWM, WDT, Input Capture, Output Compare Modes, I2C , Instruction set, Programming in Assembly and C.

### **Pedagogy**

Lectures/Experiential Learning

### **Course Outcome**

The students will:

- Students will learn the architecture of 8051, PIC and ARM .
- students will write an assembly and C program for 8051, PIC and ARM .
- students will write an assembly and C program for
- Students will be able to develop their own embedded platform using 8051, PIC and ARM

### **References/Readings**

1. Jivan Parab et al., Exploring C for microcontroller ( Springer 2007)

2. Lipovski G. J. Single and multiple Chip Microcontroller interfacing. Prentice Hall, USA 1998.
  3. Beginning Android 4 Application Development
  4. Professional Android 4 Application Development
- Learning Android Game Programming : A Hands-On Guide to Building Your First Android Game 1st Edition
- 5 .Jan Holler, VlasiosTsiatsis, Catherine Mulligan, Stefan Avesand,StamatisKarnouskos, David Boyle, "From Machine-to-Machine to theInternet of Things: Introduction to a New Age of Intelligence", 1st Edition, Academic Press, 2014.
6. Bernd Scholz-Reiter, Florian Michahelles, "Architecting the Internet ofThings", ISBN 978-3-642-19156-5 e-ISBN 978-3-642-19157-2, Springer
7. Vijay Madiseti and ArshdeepBahga, "Internet of Things (A Hands-onApproach)", 1st Edition, VPT, 2014.



**Course Code: ELE- 505**

**Course Title: Real Time Operating System**

**Number of Credits: 04**

**Total Hours: 60**

**Total Marks: 100**

**Effective from AY: 2022-23**

<b>Prerequisites for the course</b>		
Should have studied microprocessor and C programming at graduate level		
<b>Objectives of Course</b>		
This course is intended to:		
<ul style="list-style-type: none"><li>• To focus on concept of highlighting the various methods of improvising speed of computing machine through the operating system organization and various entity managements.</li><li>• To analyse the small embedded system developments through the Real Time Operating Systems for task management efficiency.</li><li>• Porting RTOS on embedded platform</li></ul>		
<b>Course Content</b>		
<b>Unit I</b>	<b>Introduction to Computer Organization and Architecture</b>	<b>6 Hours</b>
hardware vs. software -the virtual machine concept, the concept of von Neumann architecture, hardware components and functions, trends in hardware development, system configurations and classifications.		
<b>Unit II</b>	<b>Process Description and Control:</b>	<b>6</b>
Processes, process states, processor modes, context switching, CPU scheduling algorithms, threads		
<b>Unit III</b>	<b>Concurrency Control:</b>	<b>6</b>
Concurrent processes, critical section problem and solutions, mutual exclusion solution requirements, semaphores and monitors.		
<b>Unit IV</b>	<b>Deadlocks:</b>	<b>6</b>
Characterization, detection and recovery, avoidance, prevention		
<b>Unit V</b>	<b>Inter Process Communication</b>	<b>7</b>
classical IPC problems and solutions, IPC techniques.		
<b>Unit VI</b>	<b>The Input/Output and File Subsystem:</b>	<b>7</b>
I/O devices, controllers and channels, bus structures, I/O techniques (programmed, interrupt-driven and DMA), I/O subsystem layers. Concepts of files and directories, issues and techniques for efficient storage and access of data. I/O and file system support for graphics, multimedia, databases, transaction processing and networking.		
<b>Unit VII</b>	<b>The Memory Subsystem</b>	<b>7</b>
Memory types and hierarchy, module level Organization, cache memory. Memory partitioning, swapping, paging, segmentation, virtual memory.		
<b>Unit VIII</b>	<b>The Central Processing Unit</b>	<b>7</b>
CPU components, register sets, instruction cycles, addressing modes, instruction sets, the concept of micro-programming, Basics of RISC approach, pipelined and super-scalar approaches, vector processors and parallel processors, hardware support for the OS.		
<b>Unit IX</b>	<b>µCOS case study</b>	<b>8</b>

<b>Pedagogy</b>
Lectures/Experiential Learning
<b>Course Outcome</b>
<p>The Student Will:</p> <ul style="list-style-type: none"> <li>● Generalize the understanding of the computing machine and various entities associated with the enhancement of the efficiency.</li> <li>● Handle the operating system management process, memory, I/O, Secondary Disk and organizations of various.</li> <li>● Handle any operating system for process and task managements if follows the documentations of the same.</li> </ul>
<b>References/Readings</b>
<ol style="list-style-type: none"> <li>1. Operating system principles, 3rd Edition, by Willian Stallings –PHI(1998)</li> <li>2. Operating system concepts by Silberchatz and Galvin - Addison wesley</li> <li>3. Operating system by Tanaumbuam, PHI New Delhi</li> </ol>

[\(Back to Index\)](#)

**Course Code: ELE-506**

**Course Title: Digital System Design**

**Number of Credits: 04**

**Total Hours: 60**

**Total Marks: 100**

**Effective from AY: 2022-23**

<b>Prerequisites for the course</b>		
Should have studied digital electronics at the graduate level		
<b>Objectives of Course</b>		
This course is intended to, <ul style="list-style-type: none"><li>• Teach principles of combination and sequential logic design</li><li>• Develop implementation skills using hardware description languages.</li><li>• Teach and familiarize with industry technologies such as Memory, CPLDS, FPGA.</li></ul>		
<b>Course Content</b>		
<b>Unit I</b>	<b>Introduction</b>	<b>6 Hours</b>
About Digital Design, Analog versus Digital, Electronic Aspects of Digital Design, PLD's, ASIC, Digital Design level. Digital Concept and Number System: General Positional number system conversions, Operation, BCD, Gray Code, Character Codes, Codes for Actions, Conditions, and States nCubes and Distance, Codes for Detecting and Correcting Errors, Error-Detecting Codes, Error-Correcting and Multiple Error-Detecting Codes, Hamming Codes, CRC Codes, Two Dimensional Codes, Checksum Codes, m-out-of-n Codes, Codes for Serial Data Transmission and Storage, Parallel and Serial Data, Serial Line Codes		
<b>Unit II</b>	<b>Combinational Logic Design Principles</b>	<b>4 Hours</b>
Switching Algebra, Combinational-Circuit Analysis, Combinational-Circuit Synthesis, and Timing Hazards.		
<b>Unit III</b>	<b>Hardware Description Languages</b>	<b>4 Hours</b>
HDL-Based Digital Design, The VHDL Hardware Description Language, The Verilog Hardware Description Language		
<b>Unit IV</b>	<b>Combinational Logic Design Practices</b>	<b>4 Hours</b>
Documentation Standards, Circuit Timing, Combinational PLDs, Decoders, Encoders, Three-State Devices , Multiplexers, Exclusive-OR Gates and Parity Circuits , Comparators, Adders, Subtractors, and ALUs , Combinational Multipliers, Exclusive-OR Gates and Parity Circuits , Comparators, Adders, Subtractors, and ALUs , Combinational Multipliers.		
<b>Unit V</b>	<b>Sequential Logic Design Principles &amp; Practices</b>	<b>12 Hours</b>

Bistable Elements, Latches and Flip-Flops, Clocked Synchronous State-Machine Analysis, Clocked Synchronous State-Machine Design, Designing State Machines Using State Diagrams, State-Machine Synthesis Using Transition Lists, Another State-Machine Design Example, Decomposing State Machines, Feedback Sequential-Circuit Analysis, Feedback Sequential-Circuit Design, Features ,Sequential-Circuit Design with VHDL , Sequential- Circuit Design with Verilog, Sequential-Circuit Documentation Standards , Latches and Flip-Flops ,Sequential PLDs , Counters, Shift Registers, Iterative versus Sequential Circuits , Synchronous Design Methodology , Impediments to Synchronous Design , Synchronizer Failure and Metastability		
<b>Unit VI</b>	<b>Memory, CPLDS</b>	<b>10 Hours</b>
Read-Only Memory, Read/Write Memory, Static RAM, Dynamic RAM, Complex Programmable Logic Device		
<b>Unit VII</b>	<b>Field Programmable Gate Array</b>	<b>10 Hours</b>
Introduction, FPGA Architectures, Configuration: SRAM-Based FPGAs and Antifuse Permanently Programmed FPGAs, Chip I/O, Circuit Design of FPGA Fabrics, Architecture of FPGA Fabrics, FPGA Soft-core Processor Development flow.		
<b>VIII</b>	<b>Neural Networks on FPGA</b>	<b>10 Hours</b>
Introduction, Designing a Neuron, Activation functions, Design of layers, Training and validations, Hardware verification, Case study using PYNQ/VITIS AI framework.		
<b>Pedagogy</b>		
lectures/ Experiential Learning		
<b>Course Outcome</b>		
The student will, <ul style="list-style-type: none"> <li>● Understand principles of combination and sequential logic design</li> <li>● Leverage Hardware description languages for realization of combinational and sequential designs</li> <li>● Understand the architecture of field programmable gate array.</li> </ul>		
<b>References/Readings</b>		

1. Digital Design Principles and Practices, by John F. Wakerly, Prentice Hall's Fourth Edition.
2. Digital System Design using VHDL: Charles. H.Roth ; PWS (1998)
3. Scott Hauck and Andre DeHon , Reconfigurable Computing, Morgan Kaufmann, 2008
4. Srinivas Devadas, Abhijit Ghosh, and Kurt Keutzer, "Logic Synthesis," McGraw-Hill, USA, 1994.
5. Neil Weste and K. Eshragian,"Principles of CMOS VLSI Design: A System Perspective,2nd edition, Pearson Education, 2000.
6. Kevin Skahill, "VHDL for Programmable Logic," Pearson Education, 2000. M.N.O. Sadiku, Elements of Electromagnetics 2nd Edition) , Oxford University press, 1995.

**Course Code: ELE-507**

**Course Title: ELECTRONICS PRACTICALS – II**

**Number of Credits: 04**

**Total Hours: 120**

**Total Marks: 100**

**Effective from AY: 2022-23**

<b>Prerequisites for the course</b>	
Should have studied microcontrollers, embedded system, OS and EDA tools	
<b>Objectives of Course</b>	
This course is intended to, <ul style="list-style-type: none"><li>• Develop skills in handling controllers like 89C51/52, PIC and ARM controller derivatives .</li><li>• Input Output operation, Various communication interfaces, data acquisition, task management and Robotic applications.</li><li>• Cover experiments using LabVIEW with MyRIO and NI ELVIS Platform</li></ul>	
<b>Course Content</b>	
<b>Practical Title</b>	<b>120 Hours</b>
<b><u>Unit I</u></b> <ol style="list-style-type: none"><li>1. 7-segment Interfacing to ATMEL 89C52 (BCD counter)</li><li>2. Display Temperature using ATMEL 89C52 on LCD</li><li>3. Obstacle Avoidance using 89V52 based Robot</li><li>4. Serial Transmission and reception PIC16F877</li><li>5. Configuring On – chip ADC PIC16F877</li><li>6. Hex Keypad Interfaced to ARM controller &amp; display on LCD</li></ol>	
<b><u>Unit II</u></b> <ol style="list-style-type: none"><li>7. Switching of LED using <math>\mu</math> - COS</li><li>8. Switching of LED using RTX</li><li>9. Switching of LED using FPGA</li><li>10. KEY pad and ADC interfacing using RTOS</li><li>11. Shell programming – Web Application.</li><li>12. Shell programming – System Management</li></ol>	
<b><u>Unit III</u></b> <ol style="list-style-type: none"><li>13. VHDL implementation for the Multiplexer &amp; Demultiplexer</li><li>14. VHDL Implementation for Encoder &amp; Decoder</li><li>15. VHDL implementation for the Counter.</li><li>16. LCD and 7 -segment Interfacing using DE2 board</li><li>17. UART Interface using DE2 board</li><li>18. Echo &amp; Reverberation implementation on speedy33 kit(lab view)</li></ol>	
<b><u>Unit IV</u></b> <ol style="list-style-type: none"><li>19. Automated Street lighting</li><li>20. Smart Irrigation System</li><li>21. Home Automation</li><li>22. Smart water monitoring system</li></ol>	

23. Surveillance System 24. Smart Parking System			
<b>Unit V</b> 25. Switch basic setup 26. Virtual LAN 27. Spanning tree protocol 28. Routing 29. DHCP ( Dynamic Host Configuration Protocol ) 30. Switch stacking			
<b>Pedagogy</b>			
Presentations /Experiential Learning/laboratory design and implementation			
<b>Course Outcome</b>			
The Students will be, <ul style="list-style-type: none"> <li>● Able to analyze the architectures of any controller.</li> <li>● Designs application using embedded system using tasks for real time applications.</li> <li>● Handle any computing machine using shell script for computing and management.</li> <li>● Develop and design some applications based on SPEEDY 33 using LABView , NI ELVIS , MYRio, Altera DE2 Board.</li> <li>● Develop an android app.</li> </ul>			
<b>References/Readings</b>			
<ol style="list-style-type: none"> <li>1. Digital Design Principles and Practices, by John F. Wakerly, Prentice Hall's Fourth Edition. Lipovski G. J. Single and multiple Chip Microcontroller interfacing. Prentice Hall, USA 1998.</li> <li>2. Beginning Android 4 Application Development</li> <li>3. Professional Android 4 Application Development</li> <li>4. Learning Android Game Programming : A Hands-On Guide to Building Your First Android Game 1st Edition</li> </ol>			

**Course Code: ELE-523**

**Course Title: Internet of Things**

**Number of Credits: 04**

**Total Hours: 60**

**Total Marks: 100**

**Effective from AY: 2022-23**

<b>Prerequisites for the course</b>		
Students should know the basic knowledge about passive electrical and electronics components, and programming		
<b>Objectives of Course</b>		
This course is intended to:		
<ul style="list-style-type: none"><li>● Introduce the fundamentals of Internet of Things and its building blocks along with their characteristics.</li><li>● understand the protocols and standards designed for IoT and the current research on it.</li><li>● know the other associated technologies like cloud and fog computing in the domain of IoT.</li><li>● provide the recent application domains of IoT in everyday life.</li></ul>		
<b>Course Content</b>		
<b>Unit I</b>	<b>Introduction</b>	<b>05 Hours</b>
Evolution, Addressing strategies, Sensing and Actuation -Type, Characteristics, Processing topologies and types		
<b>Unit II</b>	<b>IOT Networking</b>	<b>10 Hours</b>
Basics of Networking, Networking Components, Connectivity Protocol: 6LoWPAN and RFID, Data Protocol – MQTT, SMQTT, CoAP, XMPP and AMQP, Communication protocols – IEEE 802.15.4, Zigbee, HART & Wireless HART, NFC, Bluetooth, Z-wave and ISA 100.11A		
<b>Unit III</b>	<b>Sensing Network</b>	<b>15 Hours</b>
Wireless Sensor Networks, Sensor nodes, Sensor web, Node Behavior in WSNs, Applications of WSNs, Coverage of WSNs, Stationary and Mobile Wireless Sensor Network, UAV Network, Flying Ad Hoc Network, Interoperability		
<b>Unit IV</b>	<b>Software Defined Networking</b>	<b>10 Hours</b>
Basic concept, SDN architecture, SDN in IOT, Software Defined WSN, SDN for Mobile Networking		
<b>Unit V</b>	<b>Cloud and Fog Computing</b>	<b>10 Hours</b>
Cloud Computing: Fundamentals, Components & Characteristics, Architecture, Service Models, Cloud types, Service Management & Security and Sensor Cloud, Fog Computing: Fog nodes, Architecture, Fog Computing in IOT and Application		



<b>Unit VI</b>	<b>IOT case studies and Future Trends</b>	<b>05 Hours</b>
Smart Cities and Smart Homes, Connected Vehicles, Smart Grid, Industrial IOT, Agriculture, Healthcare, Paradigms, Challenges and the future.		
<b>Unit VII</b>	<b>Hands-on</b>	<b>05 Hours</b>
Integration of sensors and actuators with Arduinio, Introduction to Python programming, Introduction to Raspberry Pi, Implementation of IOT with Raspberry Pi		
<b>Pedagogy</b>		
Lectures/Experiential Learning		
<b>Course Outcome</b>		
<p>On completion of the course, students will be able to:</p> <ol style="list-style-type: none"> <li>1. Explain the of IOT enabling components, such as sensors, connectivity protocols, and communication protocols.</li> <li>2. Describe the IOT architecture and its component details.</li> <li>3. Explain the associated technologies including cloud computing, fog computing and its applications.</li> <li>4. Gain practical knowledge about the integration of sensor and actuators, coding structure, and implementation of IOT in various applications.</li> </ol>		
<b>References/Readings</b>		
<ol style="list-style-type: none"> <li>1. S. Misra, A. Mukherjee, and A. Roy, 'Introduction to IoT', Cambridge University Press.</li> <li>2. S. Misra, C. Roy, and A. Mukherjee, 'Introduction to Industrial Internet of Things and Industry 4.0', CRC Press.</li> <li>3. Pethuru Raj and Anupama C. Raman, 'The Internet of Things: Enabling Technologies', Platforms, and Use Cases", CRC Press.</li> <li>4. Arshdeep Bahga and Vijay Madisetti, 'Internet of Things: A Hands-on Approach', Universities Press.</li> </ol>		

**Course Code: ELE-524**

**Course Title: Switching and Routing**

**Number of Credits: 04**

**Total Hours: 60**

**Total Marks: 100**

**Effective from AY: 2022-23**

<b>Prerequisites for the course</b>		
Students should know the basic knowledge about network, basic terminologies and security at graduate level		
<b>Objectives of Course</b>		
This course is intended to: <ul style="list-style-type: none"><li>● Introduce the student to the broader understanding of computer networks</li><li>● Cover Extensive learning in switching and routing technologies.</li><li>● Comprehensive understanding in LAN switching environment.</li></ul>		
<b>Course Content</b>		
<b>Unit I</b>	<b>Computer Networking</b>	<b>05 Hours</b>
Introduction, Network Components, Network types: LAN, PAN, MAN & WAN, Connection type: Point-to-point & Point-to-multipoint, Physical Topology: Star, Mesh, Bus and Ring, Data Communication		
<b>Unit II</b>	<b>Network Models</b>	<b>10 Hours</b>
OSI Model, TCP/IP Model, OSI and TCP/IP model comparison, Critique of OSI and TCP/IP model, Internet and Internet Architecture		
<b>Unit III</b>	<b>IP Addressing and Subnets</b>	<b>15 Hours</b>
IP Addressing: Composition, Types & Classes, Private and Public IP addresses, Subnetting, Variable Length Subnet Masks (VLSM), Troubleshooting IP addressing		
<b>Unit IV</b>	<b>Switching and Spanning Tree Protocol</b>	<b>15 Hours</b>
Switching and Switches, Switch Operation, Virtual Local Area Network (VLAN) and VLAN Trunking Protocol (VTP), Spanning Tree Protocol (STP), Switch Stacking, Network Address Translation (NAT)		
<b>Unit V</b>	<b>Routing</b>	<b>10 Hours</b>
IP Routing, Types, Classes of Routing, Distance Vector Routing Protocol (DVRP), Routing Information Protocol (RIP)		
<b>Unit VI</b>	<b>Network Security</b>	<b>05 Hours</b>
Cryptography, Digital Signatures, Communication Security, Web Security, Virtual Private Network (VPN),		
<b>Pedagogy</b>		

Lectures/Experiential Learning
<b>Course Outcome</b>
The students will: <ul style="list-style-type: none"><li>● Explain in detail the concepts of Computer networking, OSI and TCP/IP model architecture along with the comparison.</li><li>● Gain the knowledge about the Switching and Routing</li><li>● Understand the basic device configuration and troubleshooting.</li><li>● Explain the Network security principles and its applications</li></ul>
<b>References/Readings</b>
<ol style="list-style-type: none"><li>1. Andrew S.Tanenbaum, 'Computer Networks', 3rd Edition, Prentice Hall.</li><li>2. James F. Kurose and Keith W. Ross, 'Computer Networking: A Top-Down Approach', 7th Edition, Pearson Education.</li><li>3. D-Link Certified, DCS Switching Training Guide.</li><li>4. D-Link Certified, DCS Switching Lab Manual</li><li>5. Cisco Certified Network Associate Training Guide</li></ol>

### Semester III

**Course Code: ELE-600**

**Course Title: Signals and Systems**

**Number of Credits: 04**

**Total Hours: 60**

**Total Marks: 100**

**Effective from AY: 2022-23**

<b>Prerequisites for the course</b>		
Should have the basic knowledge of Integration, Differentiation, Complex Numbers		
<b>Objectives of Course</b>		
This course is intended to: <ul style="list-style-type: none"><li>• Develop understanding about signals, systems and their classification.</li><li>• Provide with necessary tools and techniques to analyse electrical networks and systems to develop expertise in time-domain and frequency domain approaches.</li><li>• Discusses different types of Filters and its design.</li></ul>		
<b>Course Content</b>		
<b>Unit I</b>	<b>Signals and Signal Processing</b>	<b>05 Hours</b>
Characterization and Classification of Signals, Typical Signal Processing Operations.		
<b>Unit II</b>	<b>Discrete Time Signal and Systems</b>	<b>08 Hours</b>
Discrete-Time Signals, Sequence Representation, Sampling Process, Simple Interconnection Schemes, Correlation of Signals, Random Signal.		
<b>Unit III</b>	<b>Discrete Transform</b>	<b>17 Hours</b>
Fourier series, Continuous and Discrete-time Fourier Transform, Laplace Transform, Energy Density Spectrum, Phase and Group Delays. Digital Processing of Continuous Time Signals - Sampling of Continuous Time Signal, Low-pass & Band-pass Signal, Anti-Aliasing Filter design, Sample-and-Hold (S/H), Analog to Digital, Digital to Analog Convertors, Effects of S/H, Short-Time Fourier Transform, Wavelet Transform		
<b>Unit IV</b>	<b>Digital Filter Structure</b>	<b>08 Hours</b>
Block Diagram Representation, FIR, IIR filter, Allpass Filter, Tunable IIR Digital Filter, Digital Sin-Cosine Generator, Computational Complexity.		
<b>Unit V</b>	<b>FIR Digital Filter Design</b>	<b>10 Hours</b>
Preliminary Considerations, FIR Filter Design Based on Windowed Fourier Series, Design of Minimum Phase FIR Filters.		
<b>Unit VI</b>	<b>DSP Algorithm Implementation</b>	<b>08 Hours</b>
Computability Equation Describing Filter Structure, Verification, Computation of Discrete Fourier Transform (DFT), FFT, DFT & Inverse-DFT using MATAB, Number Representation, Handling Overflow, Tunable Digital Filters.		
<b>Unit VII</b>	<b>Application of Digital Signal Processing</b>	<b>04 Hours</b>
Dual-Tone Multi-Frequency Signal Detection, Musical Sound Processing, Signal Compression, Transmultiplexers		
<b>Case Studies</b>		
<ol style="list-style-type: none"><li>1. Implementation of Filters – Ensemble Average filter, Exponential weighted running system, Median Filter.</li><li>2. Implementation of aliasing effect and interpolation</li><li>3. Implementation of DFT and FFT algorithms</li><li>4. Design Oscillator system - Lorentz and Gaussian oscillator system</li><li>5. Design Transfer function and convolution</li></ol>		

<b>Pedagogy</b>
lectures/ Experiential Learning
<b>Course Outcome</b>
<p>Students will be able to:</p> <ol style="list-style-type: none"> <li>1. Explain classification of signals and signal processing operations.</li> <li>2. Understand the discrete time signals and its discrete time Fourier transform representation.</li> <li>3. Learn different structural representation of FIR and IIR digital filters.</li> </ol>
<b>References/Readings</b>
<ol style="list-style-type: none"> <li>1. Sanjit K Mitra, 'Digital Signal Processing: A computer Based Approach, 3rd Edition, Tata MacGraw-Hill, 2011</li> <li>2. Johnny R. Johnson, Introduction To Digital Signal Processing, Prentice Hall, 1989.</li> <li>3. John G. Proakis, Dimitris G. Manolakis, Digital Signal Processing, 4th Edition, Pearson Education, 2007.</li> <li>4. Simon Haykin, Van Veen, Signals and Systems, 2nd Edition, Wiley, 2007</li> <li>5. Richard G. Lyons, 'Understanding Digital Signal Processing', Pearson, 2022</li> <li>6. S. Palani, 'Signals and Systems', Springer International Publishing, 2021</li> </ol>

**Course Code: ELE-601**

**Course Title: Artificial Intelligence and Applications**

**Number of Credits: 04**

**Total Hours: 60**

**Total Marks: 100**

**Effective from AY: 2022-23**

<b>Prerequisites for the course</b>		
Should have knowledge of basic electronics and programming.		
<b>Objectives of Course</b>		
This course is intended to: <ul style="list-style-type: none"><li>• Introduce the foundation concepts in the field of artificial intelligence.</li><li>• Become familiar with basic principles of AI toward problem solving</li><li>• Know approaches of inference, perception, uncertain knowledge, and reasoning</li></ul> Prepares a student to take a variety of focused, advanced courses in various subfields of AI.		
<b>Course Content</b>		
<b>Unit I</b>	<b>Introduction</b>	<b>08 Hours</b>
History, Philosophy of AI, Definitions, Introduction to: <ul style="list-style-type: none"><li>• AI System Hardware CPU, RAM, GPU, Interconnects, Storage, Network Controller;</li><li>• AI Accelerators GPUs;</li><li>• System Software Operating System, Virtualization, Cloud.</li></ul>		
<b>Unit II</b>	<b>Containers, IDE &amp; Schedulers</b>	<b>08 Hours</b>
Introduction to Containers and IDE; Scheduling and Resource Management Introduction to schedulers and orchestration tools; DeepOps: Deep-dive into Kubernetes with the deployment of various AI-based services.		
<b>Unit III</b>	<b>Problem-solving by search</b>	<b>12 Hours</b>
Introduction to unguided and guided search; State Space Search: Depth First Search, Breadth First Search, Depth First Iterative Deepening Heuristic Search: Best First Search, Hill Climbing, Solution Space, TSP, Escaping Local Optima, Stochastic Local Search		
<b>Unit IV</b>	<b>Knowledge Representation and Reasoning</b>	<b>10 Hours</b>
Knowledge Representation (KR) Introduction, Knowledge Progression, KR model, category: typology map, type, relationship, framework, mapping, forward & backward representation, KR system requirements; KR schemes; KR issues; KR using predicate logic, KR using Rules Reasoning: Definitions Reasoning, formal logic, and informal logic, uncertainty, monotonic logic, non-monotonic Logic; Methods of reasoning and examples – deductive, inductive, abductive, analogy; Sources of uncertainty; Approaches to reasoning – symbolic, statistical		

and fuzzy; Symbolic Reasoning, Statistical Reasoning		
<b>Unit V</b>	<b>Learning System &amp; Neural Networks</b>	<b>12 Hours</b>
Definition, learning agents, components of the learning system; Rote Learning, Learning from examples; Explanation-based learning, Clustering, Reinforcement Learning Introduction to NN, ANN, CNN, ML, Deep Learning: Model, architecture, Learning Methods		
<b>Unit VI</b>	<b>Applications of AI</b>	<b>10 Hours</b>
Game playing, Computer Vision, Expert Systems, agricultural and soil management applications, Cognitive Science, Finance, meteorology, Health care.		
<b>Case Studies:</b>		
<ol style="list-style-type: none"> <li>1. Implementation of CNN for databases available in the public domain.</li> <li>2. Development of your own deep network.</li> <li>3. Image processing of the data collected using UAV/drone</li> <li>4. Solving health care/ meteorology problem using AI algorithm.</li> </ol>		
<b>Pedagogy</b>		
Lectures/Experiential Learning		
<b>Course Outcome</b>		
<p>Students will:</p> <ul style="list-style-type: none"> <li>• Gain knowledge of the basic concepts of Artificial Intelligence.</li> <li>• Learn problem-solving, knowledge representation, and reasoning approaches.</li> <li>• Able to deal with all the concepts and problems using NN, ANN, CNN, ML, and deep learning.</li> <li>• Able to apply the knowledge and will generate automated systems (Applications) using AI.</li> </ul>		
<b>References/Readings</b>		
<ol style="list-style-type: none"> <li>1. Deepak Khemani, 'A First Course in Artificial Intelligence', McGraw Hill Education (India), 1<sup>st</sup> ED, 2014.</li> <li>2. Stuart J. Russell and Peter Norvig, 'Artificial Intelligence', Pearson, 3rd ED, 2016</li> <li>3. George F Luger, Artificial Intelligence: Structure and strategies for complex, Problem Solving, 6th ED, 2021.</li> <li>4. Wolfgang Ertel, Nathanael T. Black, Introduction to Artificial Intelligence Nils J Nilsson, Springer, 2018</li> </ol>		

5. Principles of Artificial Intelligence, Illustrated Reprint Edition, Springer Heidelberg, 2014.
6. Alexander Jung Machine, 'Learning: The Basics (Machine Learning: Foundations, Methodologies, and Applications)', Springer, 2022
7. Cherry Bhargava, Pardeep Kumar Sharma, 'Artificial Intelligence. Fundamentals and Applications', CRC Press, 2022



**Course Code: ELE-602**

**Course Title: Robotics**

**Number of Credits: 04**

**Total Hours: 60**

**Total Marks: 100**

**Effective from AY: 2022-23**

**Prerequisites for the course**

Should have knowledge of Basic electronics hardware, Mathematics, and programming.

**Objectives of Course**

This course is intended to:

- Introduce robotics and the key elements and constituents of a robot; science and technology in robots; ROS.
- Understand and explain the various elements of the robotic system.
- Study all necessary kinematics and various analysis techniques.
- Understand the robot dynamics and control theory.
- Give exposure to futuristic robotic technologies.

**Course Content**

**Unit I**

**Introduction**

**10 Hours**

Introduction- Brief history, types, classification and usage, growth; Robot applications- Manufacturing industry, defense, rehabilitation, medical, etc.; Laws of Robotics; Introduction to ROS

**Unit II**

**Elements of robots**

**12 Hours**

Links, joints, actuators, and sensors;  
Position and orientation of a rigid body, Homogeneous transformations;  
Representation of joints, link representation using D-H parameters, Examples of D-H parameters, and link transforms;  
Different kinds of actuators – stepper, DC servo and brushless motors, model of a DC servo motor;  
Types of transmissions;  
Purpose of sensors, internal and external sensors, common sensors – encoders, tachometers, strain gauge-based force-torque sensors, proximity, and distance measuring sensors, and vision sensors.

**Unit III**

**Kinematics**

**07 Hours**

Direct and Inverse Kinematics;  
Kinematics of serial robots: Direct and inverse kinematics problems, Inverse kinematics of constrained and redundant robots, Tractrix-based approach for fixed and free robots and multi-body systems;  
Kinematics of parallel robots: direct and inverse kinematics problems, Mobility, Stewart-

Gough platform, Degrees-of- freedom of parallel mechanisms and manipulators, Active and passive joints.

<b>Unit IV</b>	<b>Velocity and static analysis</b>	<b>05 Hours</b>
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Linear and angular velocity of links, Manipulator Jacobians for serial and parallel manipulators;  
Statics of serial and parallel manipulators, Statics and force transformation matrix of a GoughStewart platform, Singularity and Statics analysis.

<b>Unit V</b>	<b>Robot Dynamics &amp; Controls</b>	<b>10 Hours</b>
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Robot dynamics equation; General procedure for dynamics equation forming and introduction to control; Actuator dynamics and PD, PID control for robots; Trajectory tracking control; Lyapunovs theorems; Neural network control design

<b>Unit VI</b>	<b>Robotics Applications</b>	<b>09 Hours</b>
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Introduction, development, and working of: UAV, Drone, Humanoid Robots & Underwater robot

<b>Unit VII</b>	<b>Futuristic Robots</b>	<b>07 Hours</b>
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Introductions to MEMS (micro-electro-mechanical systems),  
Introduction to Cognitive Robotics and Human-Robot Interaction,  
Robots in Space & Defense applications

**Case Studies:**

1. Development of obstacle avoidance and line following robot.
2. Implementation of ROS.
3. Simulation of robotic trajectories.
4. Drone-based image analysis.

**Pedagogy**

Lectures/Experiential Learning

**Course Outcome**

Students will:

- Gain knowledge of the basic concepts in robotics, ROS, key elements, and constituents of the robotic system.
- Learn the kinematics of serial and parallel robots and will be able to perform various analyses.
- Understand the various robotic dynamics and control designs.
- Able to understand and develop robotic systems.
- Have an update on the latest robotic research & technologies.

#### **References/Readings**

1. Ghosal, A., 'Robotics: Fundamental Concepts and Analysis', Oxford University Press, 9<sup>th</sup> reprint, 2013
2. Robert J Schilling, 'Fundamentals of Robotics', Prentice Hall India, 1<sup>st</sup> ED, 2003
3. John J Craig, 'Introduction to Robotics', Prentice Hall International, 3<sup>rd</sup> ED, 2005
4. Jitendra R. Raol, Ramakalyan Ayyagari, 'Control Systems: Classical, Modern, and AI-Based Approaches', CRC Press, 1<sup>st</sup> ED, 2019
5. Gao, Yang, 'Space Robotics and Autonomous Systems', Institution of Engineering & Technology, 2021
6. Lentin Joseph, Aleena Johny, 'Robot Operating System (ROS) for Absolute Beginners', 2<sup>nd</sup> ED, 2022

[\(Back to Index\)](#)

**Course Code: ELE-621**

**Course Title: Laser System Engineering**

**Number of Credits: 04**

**Total Hours: 60**

**Total Marks: 100**

**Effective from AY: 2022-23**

<b>Prerequisites for the course</b>		
Graduate level knowledge in Electronics/Physics		
<b>Objectives of Course</b>		
This course is intended to: <ul style="list-style-type: none"><li>● Teach the difference between ordinary light and light emitted by a laser device.</li><li>● Introduce the method used for excitation of laser devices.</li><li>● Explain the theory behind generation of stimulated emissions.</li><li>● Cover various application of lasers in medical, civil and defence areas.</li></ul>		
<b>Course Content</b>		
<b>Unit I</b>	<b>Optical Resonators</b>	<b>12 Hours</b>
Energies in resonator, Fabry-Perot Etalon , Fabry-Perot Etalon as Optical Spectrum Analyzer, Mode Stability Criteria , Resonance Frequency of Optical Resonator, Unstable Resonator		
<b>Unit II</b>	<b>Interaction of Radiation with Atomic System</b>	<b>10 Hours</b>
Spontaneous transmission between Atomic layer, Homogenous and In-Homogeneous broadening , Line shape functions, Stimulated transmission , Absorption and amplification , gain saturation in Homogenous media .		
<b>Unit III</b>	<b>Theory of Laser Oscillator</b>	<b>10 Hours</b>
Fabry Perot Laser , Three and Four Level Laser , Power in Laser Oscillator, Optimum Light coupling , Multimode Laser Oscillator and Mode Locking Methods of Mode locking , Pulse length Measurements , Q-Switching , methods of Q-Switching .		
<b>Unit IV</b>	<b>Laser Systems</b>	<b>8 Hours</b>
Pumping and laser Efficiency, Ruby Laser, Flash Pumping ,Nd-YAG Laser , Nd Glass Laser , Threshold for CW and Pulse operation , HeNe Laser , CO <sub>2</sub> Laser , Ar-Ion Laser , Excimer Laser , Dye Laser.		
<b>Unit V</b>	<b>Non –Linear Optics</b>	<b>6 Hours</b>

Origins of Non-Linear Polarization, relation between induced Polarization		
<b>Unit VI</b>	<b>Interaction of Light and Sound</b>	<b>7 Hours</b>
Scattering of Light by Sound, RamanNath and Bragg diffraction , Diffraction of light by Sound , Intensity modulation.		
<b>Unit VII</b>	<b>Applications of Lasers</b>	<b>7 Hours</b>
Lidar, Laser cutting and welding, lunar laser ranging, Optical Network, optical tweezers, microscopy, Laser Leveling, Surface Defects scanning, bar code scanner		
	<b>Case Studies</b>	
1. Understanding the diffraction of Laser Light using grating 2. Comparison of resolving power of Prism and Grating. 3. Focusing of Laser Light. 4. Collimation of Laser Light.		
<b>Pedagogy</b>		
lectures/ tutorials/assignments/presentation		
<b>Course Outcome</b>		
The student will, <ul style="list-style-type: none"> <li>● have sufficient knowledge of lasers for applications involving medical treatment as well as defence needs.</li> <li>● have a full knowledge of classification of lasers and its usage.</li> <li>● Have knowledge to handle high power industrial lasers.</li> </ul>		
<b>References/Readings</b>		
1. A. Yariv, "Optical Electronics", 4th Edition by, HRW publication, 1991. 2. A. Ghatak and K. Tyagarajan, "Optical Electronics", by Cambridge University Press, 1989 . 3. William T. Silfvast, "Laser fundamentals", 2 <sup>nd</sup> Edition, Cambridge University Press, 2008 4. K.Thyagarajan, and Ajoy Ghatak, Lasers: Fundamentals and Applications (Graduate Texts in Physics), Springer publication, 2012. 5. L. Tarasov, "Laser Physics and application" , Mir Publication, 1987		

**Course Code: ELE-622**

**Course Title: Data Science and Machine Learning**

**Number of Credits: 04**

**Total Hours: 60**

**Total Marks: 100**

**Effective from AY: 2022-23**

<b>Prerequisites for the course</b>		
Should have the basic knowledge of linear algebra and Python programming language.		
<b>Objectives of Course</b>		
This course is intended to: <ul style="list-style-type: none"><li>• Introduce the mathematical foundations required for Data Science and Machine learning.</li><li>• Introduce the statistical and probabilistic concepts.</li><li>• Learn the data analysis and processing.</li><li>• To study different machine learning and deep learning techniques.</li></ul>		
<b>Course Content</b>		
<b>Unit I</b>	<b>Introduction</b>	<b>05 Hours</b>
Introduction to Data Science, Data Science Process, Fundamentals of linear algebra		
<b>Unit II</b>	<b>Foundations of Data Science</b>	<b>08 Hours</b>
Python for Data Science- Programming basics, libraries – Numpy, SciPy, Scikit-Learn, Tensor flow, Keras, Py-torch, Pandas, Matplotlib, Seaborn. Statistical concepts – Descriptive and Inferential Statistics, Probability		
<b>Unit III</b>	<b>Data Visualization, Exploration and Manipulation</b>	<b>12 Hours</b>
Types of Data, Visualization, Exploratory Data Analysis, Data Cleansing, Data Manipulation, Feature Extraction and Feature Selection		
<b>Unit IV</b>	<b>Regression and Classification</b>	<b>10 Hours</b>
Linear Regression, Logistic Regression, Multivariate Regression, Support Vector Machine, Decision Tree, Random Forest, Naive Bayes, Regularization, Goodness of fit		
<b>Unit V</b>	<b>Machine Learning</b>	<b>15 Hours</b>
Machine Learning Process Flow, Overfitting and under fitting, Bias-Variance, Types of machine learning, Dimensionality Reduction-Principal Component Analysis, K-Nearest Neighbour, Clustering- K-means		
<b>Unit VI</b>	<b>Deep Learning</b>	<b>10 Hours</b>
Neural Networks, Convolutional Neural Networks, Transfer Learning, Reinforcement learning, Recurrent Neural Networks		
case studies		
<ol style="list-style-type: none"><li>1. Implement data cleansing and manipulation operations</li><li>2. Implement Support Vector Machine algorithm for multiclass classification</li><li>3. Implement clustering algorithm</li><li>4. Demonstrate the overfitting and under fitting conditions</li><li>5. Face recognition using deep convolutional neural network and using transfer learning.</li></ol>		
<b>Pedagogy</b>		
lectures/ Experiential Learning		
<b>Course Outcome</b>		
The students will: <ol style="list-style-type: none"><li>1. Understand the fundamental concepts of data science and machine learning.</li><li>2. Perform data processing technique using python.</li></ol>		

3. Explain and implement the machine learning methods.

**References/Readings**

1. Trevor Hastie, Robert Tibshirani, Jerome Friedman, 'The Elements of Statistical Learning - Data Mining, Inference, and Prediction', Springer New York, 2013
2. Joel Grus, 'Data Science from Scratch - First Principles with Python', 2nd Edition, O'Reilly Media, 2019.
3. Cathy O'Neil and Rachel Schutt, 'Doing Data Science - Straight Talk From The Frontline', O'Reilly. 2013.
4. Laura Igual, Santi Seguí, Eloi Puertas, Petia Radeva, Oriol Pujol, Sergio Escalera, Francesc Dantí, Lluís Garrido, 'Introduction to Data Science - A Python Approach to Concepts, Techniques and Applications', Springer International Publishing, 2017
5. Samir Madhavan, 'Mastering Python for Data Science', Packt Publishing, 2015
6. Ian Goodfellow, Yoshua Bengio, Aaron Courville, 'Deep Learning', MIT Press, 2016
7. Zhi-Hua Zhou, 'Machine Learning', Springer Nature Singapore, 2021

**Course Code: ELE-623**

**Course Title: Electric Vehicle Technology**

**Number of Credits: 04**

**Total Hours: 60**

**Total Marks: 100**

**Effective from AY: 2022-23**

<b>Prerequisites for the course</b>		
<b>Should have graduate level knowledge in Basic Electrical and Electronics</b>		
<b>Objectives of Course</b>		
This course is intended to: <ul style="list-style-type: none"><li>• Introduce Hybrid &amp; Electric Vehicle</li><li>• Cover various types of Electric Drives and energy</li><li>• Modelling and Characteristics of EV/HEV Powertrains Components</li><li>• Matlab Simulink modeling of Electric Vehicle</li></ul>		
<b>Course Content</b>		
<b>Unit I</b>	<b>Introduction to Hybrid Electric Vehicle</b>	<b>5 Hours</b>
Review of Conventional Vehicle, Introduction to Hybrid Electric Vehicles, Types of EVs, Hybrid Electric Drive-train, Tractive effort in normal driving		
<b>Unit II</b>	<b>Electric Drives</b>	<b>10 Hours</b>
Energy consumption Concept of Hybrid Electric Drive Trains, Architecture of Hybrid Electric Drive Trains, Series Hybrid Electric Drive Trains, Parallel hybrid electric drive trains, Electric Propulsion unit, Configuration and control of DC Motor drives, BLDC, Induction Motor drives, Permanent Magnet Motor drives, switched reluctance motor		
<b>Unit III</b>	<b>Energy Storage</b>	<b>8 Hours</b>
Introduction to Energy Storage Requirements in Hybrid and Electric Vehicles:- Battery based energy storage and its analysis, Fuel Cell based energy storage and its analysis, Hybridization of different energy storage devices. Sizing the drive system, Design of Hybrid Electric Vehicle and Plug-in Electric Vehicle		
<b>Unit IV</b>	<b>Energy Management System</b>	<b>8 Hours</b>
Energy Management Strategies, Automotive networking and communication, EV charging standards, V2G, G2V, V2B, V2H		
<b>Unit V</b>	<b>Mobility and Connectors</b>	<b>8 Hours</b>
Connected Mobility and Autonomous Mobility- case study E-mobility Indian Roadmap Perspective. Policy: EVs in infrastructure system, integration of EVs in smart grid, social dimensions of EVs.  Connectors- Types of EV charging connector, North American EV Plug Standards, DC Fast Charge EV Plug Standards in North America, CCS (Combined Charging System), CHAdeMO, Tesla, European EV Plug Standards		
<b>Unit VI</b>	<b>Modelling of Vehicle Performance Parameter</b>	<b>7 Hours</b>
Modelling Vehicle Acceleration - Acceleration performance parameters, modeling the acceleration of an electric scooter, modeling the acceleration of a small car.		
<b>Unit VII</b>	<b>Modelling of Battery Electric Vehicles</b>	<b>7 Hours</b>
Electric Vehicle Modelling - Tractive Effort, Rolling resistance force, Aerodynamic drag, Hill climbing force, Acceleration force, Total tractive effort, Modelling Electric Vehicle Range - Driving cycles, Range modelling of battery electric vehicles, Constant velocity range modelling, Range modelling of fuel cell vehicles, Range modelling of hybrid electric vehicles		
<b>UNIT VIII</b>	<b>Drivetrain Characteristics</b>	<b>7 Hours</b>
Modelling and Characteristics of EV/HEV Powertrains Components- ICE Performance		



Characteristics, Electric Motor Performance Characteristics - Battery Performance Characteristics-Transmission and Drivetrain Characteristics-Regenerative Braking Characteristics-Driving Cycles Modelling and Analysis of Electric and Hybrid Electric Vehicles Propulsion and Braking - Longitudinal Dynamics Equation of Motion - Vehicle Propulsion Modelling and Analysis - Vehicle Braking Modelling and Analysis
<b>Case studies</b>
1. Explore Electric Powertrain Architectures 2. Tune Regenerative Braking Algorithms 3. Modify Suspension Design 4. Optimize Vehicle-Level Performance 5. Model Inverters, Traction Motors, and Develop Motor Control Software 6. Model Batteries and Develop Battery management System(BMS)
<b>Pedagogy</b>
<b>Lectures/Experiential Learning</b>
<b>Course Outcome</b>
Students will, <ul style="list-style-type: none"> <li>• To understand about basics of hybrid electric vehicle</li> <li>• To understand about drives and control.</li> <li>• Select battery, battery indication system for EV applications</li> <li>• Design battery charger for an EV</li> <li>• Modelling of Electric Vehicle in MATLAB</li> </ul>
<b>References/Readings</b>
1. Emadi, A. (Ed.), Miller, J., Ehsani, M., Boca Raton, "Vehicular Electric Power Systems" CRC Press, 2003 2. Husain, I. Boca Raton, "Electric and Hybrid Vehicles", CRC Press, 2010. 3. Larminie, James, and John Lowry, "Electric Vehicle Technology Explained" John Wiley and Sons, 2012 4. Tariq Muneer and Irene IllescasGarcía, "The automobile, In Electric Vehicles: Prospects and Challenges", Elsevier, 2017 5. Sheldon S. Williamson, "Energy Management Strategies for Electric and Plug-in Hybrid Electric Vehicles", Springer, 2013 6. Amir Khajepour, Saber Fallah and Avesta Goodarzi, "Electric and Hybrid Vehicles Technologies, Modelling and Control: A Mechatronic Approach" John Wiley & Sons Ltd, 2014. 7. Antoni Szumanowski, "Hybrid Electric Power Train Engineering and Technology: Modelling, Control, and Simulation" IGI Global, 2013.

[\(Back to Index\)](#)

**Course Code: ELE-624**

**Course Title: Biomedical Instrumentation**

**Number of Credits: 04**

**Total Hours: 60**

**Total Marks: 100**

**Effective from AY: 2022-23**

<b>Prerequisites for the course</b>		
<b>Graduate level knowledge in analog and digital electronics.</b>		
<b>Objectives of Course</b>		
This course is intended to: · <ul style="list-style-type: none"><li>● Introduce fundamentals of biomedical instrumentation and measurements</li><li>● Introduce concepts of biotelemetry and non-invasive diagnostic imaging</li><li>● Cover noise reduction technique in electronics systems</li></ul>		
<b>Course Content</b>		
<b>Unit I</b>	<b>Introduction to biomedical instrumentation</b>	<b>10 Hours</b>
Components of Man-Measurement system, Physiological system of body, problems encountered in measuring a living System, Basic transducer principle, Source of Bioelectric Potential, Skin contact Impedance, Electrodes: ECG, EEG, EMG, Microelectrodes.		
<b>Unit II</b>	<b>Noise reduction technique in electronics systems</b>	<b>10 Hours</b>
Introduction, cabling, grounding, balancing and filtering, shielding, contact protection, Intrinsic Noise Source, Active device Noise, and Electrostatic discharge.		
<b>Unit III</b>	<b>Cardiovascular measurements.</b>	<b>8 Hours</b>
Heart and cardiovascular system, characteristics of blood flow, Electrocardiography, measurement for Blood Pressure, ,photo-plethysmography,FunctionalNIR for brain oxygenation.		
<b>Unit IV</b>	<b>Non-invasive diagnostic imaging</b>	<b>8 Hours</b>
X-Ray, CT, MRI, fMRI, PET and SPECT, ULTRASOUND, Optical Tomography		
<b>Unit V</b>	<b>Biotelemetry</b>	<b>10 Hours</b>
Introduction to Biotelemetry, Physiological parameters Adaptable to Biotelemetry, The components of Biotelemetry System, Implantable Units, and Applications of telemetry in-Patient care.		

<b>Unit VI</b>	<b>Instrumentation for clinical laboratory</b>	<b>6 Hours</b>
The Blood, Test for Blood cells, chemical Tests, Automation of chemical Test		
<b>Unit VII</b>	<b>Applications in biomedical field</b>	<b>8 Hours</b>
Wearable devices: activity trackers, hearing aid, Electromyogram band. Laser applications in biomedical fields: Ophthalmology, dermatology, urology, Photoacoustic Tomography.		
<b>Case Studies</b>		
<ol style="list-style-type: none"> <li>1. ECG wave analysis using simulator</li> <li>2. Signal condition for EMG</li> <li>3. Signal condition for photoplethysmography</li> <li>4. Implementation of noise reduction techniques</li> </ol>		
<b>Pedagogy</b>		
lectures/ Experiential Learning		
<b>Course Outcome</b>		
<p>The student will:</p> <ul style="list-style-type: none"> <li>● Understand Fundamentals of medical instrumentation, Bioelectric potential, electrodes</li> <li>● Understand concepts of biotelemetry</li> <li>● Understand application of laser in biomedical field</li> <li>● Implement Noise reduction technique in electronics systems</li> </ul>		
<b>References/Readings</b>		
<ol style="list-style-type: none"> <li>1. Leslie Cromwell, Fred J. Weibell, and Erich A. Pfeiffer, "Biomedical instrumentation and Measurements" Prentice Hall India, 2011.</li> <li>2. R .S, Khandpur, "Handbook of Biomedical instrumentation", Tata Mc GRAW Hill, 2014.</li> <li>3. Henry W. Ott, "Noise reduction Technique in Electronic systems" , Wiley &amp; sons, 1998.</li> <li>4. Paul Suetens, "Fundamentals Of Medical Imaging", 3<sup>rd</sup> Edition, Cambridge University Press, 2017.</li> </ol>		

## SEMESTER IV

**Course Code: ELE-603**

**Course Title: Optical Communication Systems**

**Number of Credits: 04**

**Total Hours: 60**

**Total Marks: 100**

**Effective from AY: 2022-23**

### Prerequisites for the course

Should have knowledge of Electro statics and electromagnetics. Also, a basic understanding of analog and digital communication is preferable.

### Objectives of Course

This course is intended to:

- Highlight the importance of optical communication over existing copper cable and microwave communication.
- Elaborate on electromagnetic spectrum usage for various applications, from telephone to satellite communication.
- Create a strong theoretical base to understand the difference between the ray theory and wave theory approach for the passage of signals in optical fibers.
- Discuss the estimation of Noise in optical detection in detail.
- Emphasizes the industrial needs in cabling technique and type of cable used.
- Discuss different techniques of optical fiber manufacturing and their characteristic

### Course Content

Unit I	Light Propagation in Optical Fiber	14 Hours
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Introduction & Evolution of Fiber optics system;  
Geometric picture, Pulse spread due to material dispersion, loss mechanism, Theory of Optical waveguides, methods of waveguides analyses, modes in steps and graded index fiber, new types of optical fibers

Unit II	Fiber Optics Technology	08 Hours
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Fiber materials & fiber fabrication, cable design, coupling, splicing and connectors, splicing methods, connectors, fiber measurements.  
Signal Degradation: Attenuation, Losses, Distortion, Pulse broadening.

Unit III	Optical Sources & Power Launching	08 Hours
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LED and LDs, development of Laser diodes structures, transmitter circuits, Coupling efficiency of the source to fiber.  
Source to fiber power launching, fiber joints, splicing.

Unit IV	Optical detectors	09 Hours
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Photoconductive detector: biasing circuit, Commercial photoconductor, Heterodyne Detection, p-n junction & its Equivalent circuit.

Photodiode: Geometry of p-i-n, Frequency Response of Photodiodes/N ratio, Schottky photodiode.  
 Avalanche diode: Construction, S/R ratio, minimum detection of power.  
 Noise in detectors

<b>Unit V</b>	<b>Receiver sensitivity and BER</b>	<b>05 Hours</b>
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Receiver design & configuration, Receiver operations, Error sources, Receiver noise

<b>Unit VI</b>	<b>Communication System design</b>	<b>08 Hours</b>
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System requirement, System design, Link analyses, Power budgeting, Line coding (NRZ, RZ, Block codes),

<b>Unit VII</b>	<b>Advance System</b>	<b>08 Hours</b>
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TDM, Undersea fiber optics communication system, fibers in a telephone network, WDM, DWDM, OAE techniques, LAN topologies & fail safe fiber optics nodes.

### Case Studies

1. To compute Numerical aperture of a fiber.
2. Launching light in single mode fiber.
3. Computing losses in fiber link.
4. Analysing fiber networks.

### Pedagogy

Lectures/Experiential Learning

### Course Outcome

Students will:

- Have some knowledge of designing a point-to-point optical link for a given situation.
- Able to choose the right type of components for designing an optical network.
- Able to monitor signal losses during signal transmission.

### References/Readings

1. Gerd Keiser , 'Optical Fiber Communication', MGH, 5<sup>th</sup> ED, 2017
2. A. Selvarajan and et al, 'Optical Fiber Communication', TMH, 1<sup>st</sup> ED, 2002
3. A. Yariv, 'Optical Electronics', HRW publication, 4<sup>th</sup> ED, 1991
4. P. Chakrabarti, ' Optical fiber Communication' MGH, 2015
5. Reinhold Noé, 'Essentials of Modern Optical Fiber Communication', Springer, 2<sup>nd</sup> ED, 2016
6. Govind P. Agrawal, 'Fiber-Optic Communication Systems', Wiley, 5<sup>th</sup> ED, 2021

[\(Back to index\)](#)

**Course Code: ELE-604**

**Course Title: Digital Image Processing**

**Number of Credits: 04**

**Total Hours: 60**

**Total Marks: 100**

**Effective from AY: 2022-23**

<b>Prerequisites for the course</b>		
Concepts of Digital Signal Processing		
<b>Objectives of Course</b>		
<ul style="list-style-type: none"><li>• Learn fundamentals of digital image processing.</li><li>• To study image processing techniques such as image enhancement, reconstruction, segmentation, morphing and representation</li></ul>		
<b>Course Content</b>		
<b>Unit I</b>	<b>Introduction</b>	<b>10 Hours</b>
Digital image fundamentals, Sampling and quantization, Pixel relationship, Imaging geometry, Image transforms		
<b>Unit II</b>	<b>Image Enhancement</b>	<b>10 Hours</b>
Spatial domain- Gray level transformations, Histogram processing, Smoothing and sharpening Spatial filtering. Frequency domain- Fourier Transform, Smoothing and sharpening, Highpass and lowpass filters		
<b>Unit III</b>	<b>Image Restoration and Segmentation</b>	<b>10 Hours</b>
Image Restoration - Degradation/Restoration process, Noise model, Restoration approach, Inverse filtering, Weiner filtering. Segmentation - Detection of discontinuities, Edge linking and boundary detection, Region based segmentation		
<b>Unit IV</b>	<b>Color Depth and Image Processing Across Spectrum</b>	<b>10 Hours</b>
Color Image Processing - Color models, Color transformations, Smoothing and sharpening, Segmentation. Depth Imaging processing – RGBD image processing, filling holes. Image Processing across spectra- Multi-spectral imaging, Hyperspectral Imaging, Image feature extraction, fusion.		
<b>Unit V</b>	<b>Morphological Image Processing and Image Compression</b>	<b>10 Hours</b>
Morphological Image Processing – Dilation and Erosion, Hit or Miss transform, Morphological algorithms. Image Compression – Lossy and Lossless compression, Image compression standards.		
<b>Unit VI</b>	<b>Object Representation, Description and Recognition</b>	<b>10 Hours</b>
Representation, Boundary descriptors, Regional descriptor, Patterns and Pattern classes, Matching		
<b>Case studies</b>		
<ol style="list-style-type: none"><li>1. Image enhancement in spatial and frequency domain</li><li>2. Implementing degradation and restoration of image</li><li>3. Implement feature extraction algorithms</li><li>4. Object detection from the image</li><li>5. Spatio-spectral image fusion</li></ol>		
<b>Pedagogy</b>		
Lectures/ Experiential Learning		
<b>Course Outcome</b>		

The Students will:

1. Understand the fundamentals of digital image processing.
2. Operate on images using the techniques such as smoothing, sharpening and enhancement.
3. Explain the concept of image restoration, segmentation and Morphological image processing algorithms.
4. Explain object representation along with the pattern matching.

**References/Readings**

1. Rafael C Gonzalez and Richard E Woods, 'Digital Image Processing', 4th Edition, Pearson, 2018
2. Anil K Jain, 'Fundamentals of Digital Image Processing', Prentice Hall India Learning Private Limited, 1994
3. William K Pratt, 'Digital Image Processing', 4th Edition, A Wiley-Interscience Publication, 2006
4. Edward R. Dougherty, 'Digital Image Processing Methods', CRC Press, 2020
5. Kumar Navulur, 'Multispectral Image Analysis Using the Object-Oriented Paradigm', CRC Press, 2020
6. Paul L. Rosin, Yu-Kun Lai, Ling Shao, Yonghuai Liu, 'RGB-D Image Analysis and Processing', Springer International Publishing, 2020
7. D. Sundararajan, 'Digital Image Processing - A Signal Processing and Algorithmic Approach', Springer Nature Singapore, 2017

**Course Code: ELE-605**

**Course Title: Neuromorphic Computing**

**Number of Credits: 04**

**Total Hours: 60**

**Total Marks: 100**

**Effective from AY: 2022-23**

<b>Prerequisites for the course</b>		
Graduate level knowledge in analog and digital electronics. Preferable to have exposure to programming.		
<b>Objectives of Course</b>		
This course is intended to: <ul style="list-style-type: none"><li>● Introduce Neuromorphic computing and spiking neural networks (SNN).</li><li>● Introduce operational principles and learning models for Artificial Neural Networks and Spiking Neural Networks</li><li>● Cover various Neuromorphic computing architectures</li></ul>		
<b>Course Content</b>		
<b>Unit I</b>	<b>Introduction</b>	<b>7 Hours</b>



Basics of brain-inspired computing and history of neural computing, Comparison of neuromorphic and conventional computing, Basics of linear algebra and probability theory needed for modelling of neural networks.		
<b>Unit II</b>	<b>Shallow neural networks</b>	<b>17 Hours</b>
Deep learning techniques using convolutional neural networks( AlexNet, VGG, Inception Net, GoogLeNet, and ResNet), Python programming preliminaries and Software development tools for Deep Neural Net (DNN), Shallow neural networks – Perceptron, Hopfield network, Boltzmann machine, Recurrent neural network, and Kohonen’s self-organizing map		
<b>Unit III</b>	<b>Operational principles and learning models</b>	<b>17 Hours</b>
Operational principles and learning models for Artificial Neural Networks and Spiking Neural Networks(SNN) such as spike timing dependent plasticity (STDP), Q-learning, actor-critic reinforcement learning, supervised learning, and back-propagation algorithms.		
<b>Unit IV</b>	<b>Neuromorphic computing architectures</b>	<b>11 Hours</b>
Neuromorphic computing architectures- Loihi, TrueNorth, Neurogrid, Brainchip and SpiNNaker, Commercial hardware acceleration platforms such as NVIDIA’s graphics processing unit (GPU), Google’s tensor processing unit (TPU), and Intel’s vision processing unit (VPU) and FPGA accelerators.		
<b>Unit V</b>	<b>Applications and Emerging technologies</b>	<b>8 Hours</b>
Application-specific VLSI chips capable of STDP learning, actor/critic reinforcement learning, and Q-learning, Emerging technologies in neuromorphic circuits such as memristors, spin transfer torque devices, and photonic devices.		
<b>Case Studies</b>		
1. Setup of python environment for implementation of Spiking neural network(SNN) 2. Implementation of SNN for Image classification 3. Implementation of SNN for pattern recognition. 4. Handwritten digit recognition Using STDP		
<b>Pedagogy</b>		
lectures/ Experiential Learning		
<b>Course Outcome</b>		

Students will,

- Apply concepts of neuromorphic computing in research as well as industry in various applications such as computer vision, speech processing, pattern recognition etc.
- The student will be able to pursue research in development of neuromorphic hardware.

#### **References/Readings**

1. Nan Zheng and Pinaki Mazumder, "Learning in Energy-Efficient Neuromorphic Computing: Algorithm and Architecture Co-Design", John Wiley & Sons, USA, 2019.
2. Aaron C. Courville, Ian Goodfellow, and Yoshua Bengio, "Deep Learning", MIT Press, 2015.
3. Pinaki Mazumder, Yalcin Yilmaz, Idongesit Ebong, "Neuromorphic Circuits for Nanoscale Devices", River Publishing, 2019.

**Course Code: ELE-625**

**Course Title: Project**

**Number of Credits: 16**

**Total Hours: 480**

**Total Marks: 400**

**Number of Credits: 04**

**Total Hours: 60**

**Total Marks: 100**

**Effective from AY: 2022-23**

### **Prerequisites for the course**

Understanding of Electronics in the area such as Embedded programming, Machine learning, Signals & System, IoT, Robotics.

### **Objectives of Course**

#### **This course is intended to:**

- Course train students to develops conceptual design and implementation.
- Hands on experience in H/W and S/W integration.
- Train students to plan and execute the projects and tasks.
- Train students to work in groups
- Hands on exposure to work of Technology development

### **Methodology**

The Project course will commence at beginning of the I<sup>st</sup> /III<sup>rd</sup> semester.

If student wishes to take up project in industry/Academic institution/organization, such projects can be taken over six-month period in such case the student has to complete 4(four) credit RSOC course prior to II<sup>nd</sup> /IV<sup>th</sup> semester. Such student will have to associated with the mentor from industry/Academic institution/organization who will liaison with local supervisor from parent institution.

This course is basically to apply the knowledge they have acquired during the course of study and apply them for designing a gadget/interface/module required for an electronic industry/Research & Academic Institutions.

The progress of the project is monitored by the local supervisor/guide/discipline members over the period of a month for the I<sup>st</sup> /III<sup>rd</sup> semester and fortnightly for II<sup>nd</sup> /IV<sup>th</sup> semester for internal evaluation. There will be final evaluation of the projects. The scheme of evaluation will be decided by the school discipline members.

### **Pedagogy**

Presentation /Experiential Learning/Practical implementation
<b>Course Outcome</b>
<b>Students will:</b> <ul style="list-style-type: none"><li>• The student will be exposed to the different kinds of working environments in electronic industries.</li><li>• Should able to understand product development cycle, team work, research survey, technical writing.</li></ul>



[\(back to Index\)](#)