REPORT ON INVITED EXPERT TALK

Title of the event	" Life Cycle Of Chip Design"
Date and time	19 th April 2022, 3.00pm
Mode	Offline mode
Department/School/Directorate	School of Physical And Applied Sciences
	(Electronics)
Participants	30 participants
Resource Person	Saish Amonkar, Engineering Manager, Intel
Faculty attended	5 faculty participants
Students attended	25 student participants
No of external participants	Nil
	To enhance the students knowledge on chip
The objectives / description of the activity (50 words)	designing life cycle and other industrial approaches
Copy of Flyer of the event	SCHOOL OF PHYSICAL & APPLIED SCIENCES GOAUNTVERSITY ORGANIZES INVITED TALK IN ELECTRONICS DURCE * Life Cycle Of Chip Design* * Life Cycle Of Cycle Of Chip Design* * Life Cycle Of Cycle Of
URL of the event	Dr. A.A. Gaonkar Dr. M. D. Sequeira Dr. N. T. Vetrekar
Photos (4)	

Feedback	Good
Benefit/Key outcome of the event in	 Students had an opportunity to learn: 1. About different stages involved in chip development.
terms of learning/skills/knowledge	2. Job opportunities and research scope The resource person also resolved the doubts of the students.